



PATENT

#225
2-25-97
P2

RECEIVED

FEB 24 1997

GROUP 2300

Applicant: David E. McKinley
Assignee: Zilog, Inc.
Title: "DYNAMIC POWER MANAGEMENT OF SOLID STATE MEMORIES"
Serial No.: 08/368,577 Filed: January 4, 1995
Examiner: G. Davis Group Art Unit: 2309
Attorney Docket No.: M-2689-1C US

San Jose, California
February 3, 1997

COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D. C. 20231

AMENDMENT

Sir:

In response to the Office Action of November 1, 1996 please amend the above-identified application as set forth below.

IN THE CLAIMS

Sub-IT 1. (Four Times Amended) A dynamic power management device for supplying power to a solid state memory integrated circuit, said device comprising:

power control means for supplying a variable voltage to said memory integrated circuit; and

logic control means for generating address and control signals for said memory integrated circuit and for controlling said power control means;

wherein the power control means [to] supply power to said memory integrated circuit, said power being supplied to the memory integrated circuit at a first level [to maintain memory information in said memory integrated circuit] during periods of no data access activity[,] and [to supply power] at a second level [to read and write memory information in said

LAW OFFICES OF
SKJERVEN, MORRILL,
MacPHERSON, FRANKLIN
& FRIEL

25 METRO DRIVE
SUITE 700
SAN JOSE, CA 95110
TELEPHONE
FAX (408) 283-1233

P:\DMS\9885\M-2689-1\0205273.WP

- 1 -

SER. NO. 08/368,577